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PLUS Search Results for S/N 09834013, Searched October 18, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated sear
ch
system for U.S. Patents from 1971 to the present. PLUS is a
query-by-example search system which produces a list of patents that a
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most closely related linguistically to the application searched. This
search was prepared by the staff of the Scientific and Technical
Information Center, SIRA.

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6434667 99
6445522 99

Most Frequently Occurring Classifications of Patents Returned
From A Search of 09834013 on October 18, 2004

Original Classifications
  5 710/74
  4   84/602
  2 360/46
  2 710/5

Cross-Reference Classifications
  6 711/112
  5 710/5
  4 710/105
  3   84/604
  3   84/609
  2   84/645
  2 360/75
  2 360/97.01
  2 360/99.08
  2 710/107
  2 710/266

Combined Classifications
  7 710/5
  6 710/74
  6 711/112
  4   84/602
  4 710/105
  3   84/604
  3   84/609
  2   84/645
  2 360/46
  2 360/75
  2 360/97.01
  2 360/99.08
  2 708/525
  2 710/1
  2 710/107
  2 710/22
  2 710/266
  2 710/305
  2 711/117
  2 711/122
  2 711/138
  2 712/213
  2 713/400

09834013_CLSTITLES
Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 09834013 on October 18, 2004

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7   710/5          (2 OR,  5 XR)
        Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1            INPUT/OUTPUT DATA PROCESSING
        710/5            .Input/Output command process


6   710/74         (5 OR,  1 XR)
        Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1            INPUT/OUTPUT DATA PROCESSING
        710/62           .Peripheral adapting
        710/72           ..Application-specific peripheral adapting
        710/74           ...For data storage device


6   711/112        (0 OR,  6 XR)
        Class    711 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                            SYSTEMS: MEMORY
        711/100          STORAGE ACCESSING AND CONTROL
        711/101          .Specific memory composition
        711/111          ..Accessing dynamic storage device
        711/112          ...Direct access storage device (DASD)


4   84/602         (4 OR,  0 XR)
        Class    084 :  MUSIC
        84/1             INSTRUMENTS
        84/600           .Electrical musical tone generation
        84/601           ..Data storage
        84/602           ...Digital memory circuit (e.g., RAM, ROM,
                            etc.)


4   710/105        (0 OR,  4 XR)
        Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/100          INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                            TRANSACTION PROCESSING)
        710/105          .Protocol


3   84/604         (0 OR,  3 XR)
        Class    084 :  MUSIC
        84/1             INSTRUMENTS
        84/600           .Electrical musical tone generation
        84/601           ..Data storage
        84/602           ...Digital memory circuit (e.g., RAM, ROM,
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                                            etc.)
            84/604           ....Waveform memory

   3    84/609        (0 OR, 3 XR)
            Class     084 :  MUSIC
            84/1             INSTRUMENTS
            84/600           .Electrical musical tone generation
            84/601           ..Data storage
            84/602           ...Digital memory circuit (e.g., RAM, ROM,
                                etc.)
            84/609           ....Note sequence

   2    84/645        (0 OR, 2 XR)
            Class     084 :  MUSIC
            84/1             INSTRUMENTS
            84/600           .Electrical musical tone generation
            84/601           ..Data storage
            84/645           ...MIDI (musical instrument digital interface)


   2    360/46        (2 OR, 0 XR)
            Class     360 :  DYNAMIC MAGNETIC INFORMATION STORAGE OR
                                RETRIEVAL
            360/39           GENERAL PROCESSING OF A DIGITAL SIGNAL
            360/46           .Head amplifier circuit

   2    360/75        (0 OR, 2 XR)
            Class     360 :  DYNAMIC MAGNETIC INFORMATION STORAGE OR
                                RETRIEVAL
            360/69           AUTOMATIC CONTROL OF A RECORDER MECHANISM
            360/75           .Controlling the head

   2    360/97.01     (0 OR, 2 XR)
            Class     360 :  DYNAMIC MAGNETIC INFORMATION STORAGE OR
                                RETRIEVAL
            360/88           RECORD TRANSPORT WITH HEAD STATIONARY DURING
                                TRANSDUCING
            360/97.01        .Disk record

   2    360/99.08     (0 OR, 2 XR)
            Class     360 :  DYNAMIC MAGNETIC INFORMATION STORAGE OR
                                RETRIEVAL
            360/88           RECORD TRANSPORT WITH HEAD STATIONARY DURING
                                TRANSDUCING
            360/97.01        .Disk record
            360/99.08        ..Rotational drive detail

   2    708/525       (1 OR, 1 XR)
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Class 708 : ELECTRICAL COMPUTERS: ARITHMETIC PROCESSING
AND CALCULATING
708/100   ELECTRICAL DIGITAL CALCULATING COMPUTER
708/200   .Particular function performed
708/490   ..Arithmetical operation
708/525   ...Status condition/flag generation or use

2 710/1   (1 OR, 1 XR)
    Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
                PROCESSING SYSTEMS: INPUT/OUTPUT
    710/1   INPUT/OUTPUT DATA PROCESSING

2 710/107   (0 OR, 2 XR)
    Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
                PROCESSING SYSTEMS: INPUT/OUTPUT
    710/100   INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                TRANSACTION PROCESSING)
    710/107   .Bus access regulation

2 710/22   (1 OR, 1 XR)
    Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
                PROCESSING SYSTEMS: INPUT/OUTPUT
    710/1   INPUT/OUTPUT DATA PROCESSING
    710/22   .Direct Memory Accessing (DMA)

2 710/266   (0 OR, 2 XR)
    Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
                PROCESSING SYSTEMS: INPUT/OUTPUT
    710/260   INTERRUPT PROCESSING
    710/266   .Programmable interrupt processing

2 710/305   (1 OR, 1 XR)
    Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
                PROCESSING SYSTEMS: INPUT/OUTPUT
    710/100   INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                TRANSACTION PROCESSING)
    710/305   .Bus interface architecture

2 711/117   (1 OR, 1 XR)
    Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                SYSTEMS: MEMORY
    711/100   STORAGE ACCESSING AND CONTROL
    711/117   .Hierarchical memories

2 711/122   (1 OR, 1 XR)
    Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                SYSTEMS: MEMORY
    711/100   STORAGE ACCESSING AND CONTROL

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        711/117        .Hierarchical memories
        711/118        ..Caching
        711/119        ...Multiple caches
        711/122        ....Hierarchical caches

  2  711/138        (1 OR, 1 XR)
        Class    711 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                            SYSTEMS: MEMORY
        711/100          STORAGE ACCESSING AND CONTROL
        711/117          .Hierarchical memories
        711/118          ..Caching
        711/138          ...Cache bypassing

  2  712/213        (1 OR, 1 XR)
        Class    712 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                            SYSTEMS:  PROCESSING ARCHITECTURES AND    INS
TRUCTION
                            PROCESSING
        712/209          .Decoding instruction to accommodate plural
                            instruction interpretations (e.g., differe
nt dialects,
                            languages, emulation, etc.)
        712/213          .Predecoding of instruction component

  2  713/400        (1 OR, 1 XR)
        Class    713 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                            SYSTEMS:  SUPPORT
        713/400          SYNCHRONIZATION OF CLOCK OR TIMING SIGNALS,
                            DATA, OR PULSES
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